



**C. U. SHAH UNIVERSITY, Wadhwan City**

**Name of Programme: M.Tech**

**Name of Branch: VLSI & Embedded System Design**

**Semester: III**

**w. e. f. June 2015**

Sr. No	Branch Code	Subject Code	Subject Name	Teaching Schemes (Hours)				Credits	Evaluation Schemes							
				Th	Tu	Pr	Total		Theory				Practical (Marks)		Total	
									Sessional Exam		University Exam		Internal			University
									Marks	Hours	Marks	Hours	Pr	TW		Pr
1	05	5TE03SMD1	Semiconductor Memories Design (SMD)	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
2	05	5TE03ELX1	Embedded Linux (ELX)	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
3	05	5TE03SEM1	Seminar (SEM)	00	00	04	04	02	---	---	---	---	---	20	80	100
4	05	5TE03DPR2	Mid Semester Evaluation of Dissertation Preliminary	-	-	-	-	05	---	---	---	---	---	20	80	100
5	05		Dissertation Preliminary	00	00	14	14	07	---	---	---	---	---	50	150	200
			Total	08	00	22	30	24	60	---	140	---	---	130	370	700

Th: Theory, To: Tutorial/Seminar/Field work, Pr: Practical, TW: Term Work

**Note: Theory \_\_\_\_\_ 1hr = 1 Credit,**

**Tutorial/Practical \_\_\_\_\_ 2hr = 1 Credit,**